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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/757,925
		Filing Date	January 16, 2004
		First Named Inventor	Craig Hansen
		Group Art Unit	2181
		Examiner Name	Henry Tsai
		Attorney Docket Number	43876-158
Sheet	1	of	4

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	A	US-4,509,119	04-02-1985	Gumaer	
	B	US-4,943,919	07-24-1990	Aslin	
	C	US-5,208,914	05-04-1993	Wilson	
	D	US-5,253,342	10-12-1993	Blount	
	E	US-5,256,994	10-26-1993	Langendorf	
	F	US-5,287,327	02-15-1994	Takasugi	
	G	US-5,327,570	07-05-1994	Foster	
	H	US-5,371,772	12-06-1994	Al-Khairi	
	I	US-5,375,208	12-20-1994	Pitot	
	J	US-5,477,181	12-19-1995	Li	
	K	US-5,522,054	05-28-1996	Gunlock	
	L	US-5,530,960	06-25-1996	Parks	
	M	US-5,826,106	10-20-1998	Pang	
	N	US-5,828,869	10-27-1998	Johnson	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	O	WO 97/07450	02-27-1997	Hansen		

Examiner Signature	/Jesse Moll/	Date Considered	08/04/2008
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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	P	11/2/05 Complaint for Patent Infringement (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Q	2/16/06 SCEA's Motion for Stay of Litigation Pending Reexamination (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	R	3/1/06 MicroUnity's Opposition to Defendant's Motion for Stay of Litigation Pending Reexamination (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	S	3/1/06 MicroUnity's Answer to SCEA's Counterclaim (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	T	3/6/06 SCEA's Reply Supporting Its Motion for Stay of Litigation Pending Reexamination (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	U	9/29/06 Order Denying Motion to Continue, Finding as Moot Motion to Stay (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	V	12/1/06 MicroUnity's Disclosure of Asserted Claims And Preliminary Infringement Contentions Pursuant to Local Patent Rule 3-1 and Attachments A-Z, AA, and BB (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	W	12/1/06 MicroUnity's Identification of Documents Pursuant to Local Patent Rule 3-2 (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	X	12/20/06 MicroUnity's Initial Disclosures (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Y	12/21/06 SCEA's Initial Disclosures Pursuant to the Discovery Order (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Z	1/25/07 First Amended Complaint for Patent Infringement and Exhibits A-J (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AA	2/12/07 SCEA's Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	

Examiner Signature	/Jesse Moll/	Dated Considered	08/04/2008
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		Examiner Name	Henry Tsai		
Sheet	3	of	4	Attorney Docket Number	43876-158

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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	AB	3/2/07 MicroUnity's Answer to SCEA's Counterclaim in Response to MicroUnity's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AC	3/5/07 SCEA's Invalidity Contentions & Attachments A-I (portions of G and H are Outside Counsel Eyes Only and are not included) (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AD	4/3/07 SCEA's Supplemental Disclosures (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AE	5/1/07 SCEA's Supplemental Invalidity Contentions & Attachments A-I (Exhibits A2, E2, F2, G9 and H7 are Outside Counsel Eyes Only and are not included) (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AF	5/9/07 SCEA's Amended Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AG	5/22/07 MicroUnity's LPR 4-1 Proposed List of Terms to be Construed (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AH	5/22/07 SCEA's Proposed List of Claim Terms, Phrases, Clauses, And Elements For Construction (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AI	5/29/07 MicroUnity's Answer to SCEA's Amended Counterclaim in Response to MicroUnity's First Amended Complaint (<i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AJ	11/22/06 Complaint Against Advanced Micro Devices, Inc. and Exhibits A-L (<i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; USDC for the Eastern District of Texas, Marshall Division)	
	AK	2/26/07 AMD's Answer to MicroUnity's Complaint (<i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; USDC for the Eastern District of Texas, Marshall Division)	
	AL	Advance Datasheet for MT48LC2M8S1(S) 2 Meg x 8 SDRAM, Micron Semiconductor, Inc., 2-43 to 2-84 (1994)	

Examiner Signature	/Jesse Moll/	Dated Considered	08/04/2008
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	AM	Asprey et al., "Performance Features of the PA7100 Microprocessor," IEEE Micro, 22-35 (June 1993)	
	AN	BSP and BSP Customer Attributes, Inclosure 5, Burroughs Corporation (Aug. 1, 1977)	
	AO	BSP Floating Point Arithmetic, Burroughs Corporation, (Dec. 1978)	
	AP	BSP Implementation of Fortran, Burroughs Corporation (Feb. 1978)	
	AQ	BSP, Burroughs Scientific Process, Burroughs Corporation, 1-29 (June 1977)	
	AR	Bursky, "Synchronous DRAMs Clock At 100 MHz," Electronic Design, Vol. 41, No. 4, 45-49 (Feb. 18, 1993)	
	AS	D. D. Gajski and L. P. Rubinfeld, "Design of Arithmetic Elements for Burroughs Scientific Processor," Proceedings of the 4th Symposium on Computer Arithmetic, Santa Monica, CA, 245-56 (1978)	
	AT	"System Architecture," ELXSI (2d Ed. Oct. 1983)	
	AU	"System Foundation Guide," ELXSI (1st Ed. Oct. 1987)	
	AV	L. Higbie, "Applications of Vector Processing," Computer Design, 139-45 (Apr. 1978)	
	AW	Ide et al., "A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors," IEEE Journal of Solid-State Circuits, Vol. 28, No.3, 352-61 (Mar. 1993)	
	AX	"IEEE Standard for Communicating Among Processors and Peripherals Using Shared Memory (Direct Memory Access—DMA)," IEEE (1994)	
	AY	D.A. Kuck & R. Stokes, "The Burroughs Scientific Processor (BSP)," IEEE Transactions on Computers, vol. C-31, No. 5, 363-76 (May 1982)	
	AZ	Lee, "Accelerating Multimedia with Enhanced Microprocessors," IEEE Micro, Vol. 15, No.2, 22-32 (Apr. 1995)	
	BA	Lion Extension Architecture (Oct. 12, 1991)	
	BB	"MC88110 Second Generation RISC Microprocessor User's Manual," Motorola (1991)	
	BC	Spaderna et al., "An Integrated Floating Point Vector Processor for DSP and Scientific Computing," IEEE International Conference on Computer Design: VLSI in Computers and Processors, 8-13 (Oct. 1989)	
	BD	Sprunt et al., "Priority-Driven, Preemptive I/O Controllers for Real-Time Systems," IEEE (1988)	
	BE	Thornton, "Design of a Computer-The Control Data 6600," Scott, Foresman and Company (1970)	
	BF	Watkins et al., "A Memory Controller with an Integrated Graphics Processor," IEEE, 324-36 (1993)	
	BG	Patent Family Tree of Patent 5,742,840	

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